Abstract of the Disclosure

A synchronous signal generator according to the present invention outputs a pulse with low jitter by setting the sine wave output of a crystal oscillator close to an ideal sine wave, and converting it into a pulse signal. By passing the sine wave output from the crystal oscillator of an oscillation frequency f through a filter unit having an equal center frequency f0, inputting output of a filter unit into a pulse converter, and converting the result into a pulse of a rectangular waveform, thereby obtaining an output signal. By configuring the filter unit by the crystal filter and setting it equal to the crystal oscillator in frequency-temperature characteristic, an output signal can be obtained with low jitter although the temperature changes.